



2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izuo Iida
Serial No. : 09/876,554
Filed : June 7, 2001
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : 2815
Examiner : N Drew Richards

Commissioner for Patents
Washington, D.C. 20231

#8
Electron
J. McNeill
11/26/02

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed October 23, 2002, applicant elects the invention of Group I (claims 1 to 3) drawn to a subcombination.. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

11/14/02

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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